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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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Silicon N Channel Power MOS FET High Speed Power Switching

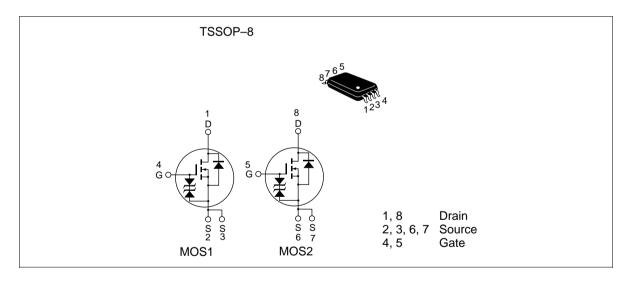


ADE-208-661A (Z) 2nd. Edition Feb. 1999

Features

- Low on-resistance
- Capable of 2.5 V gate drive
- Low drive current
- High density mounting

Outline



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	30	V
Gate to source voltage	V _{GSS}	± 10	V
Drain current	I _D	1	A
Drain peak current	Note1 D(pulse)	4	A
Body-drain diode reverse drain current	I _{DR}	1	A
Channel dissipation	Pch Note2	0.8	W
Channel dissipation	Pch Note3	1.2	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	– 55 to + 150	°C

Note: 1. $PW \le 10\mu s$, duty cycle $\le 1 \%$

2. 1 Drive operation : When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW≤ 10s

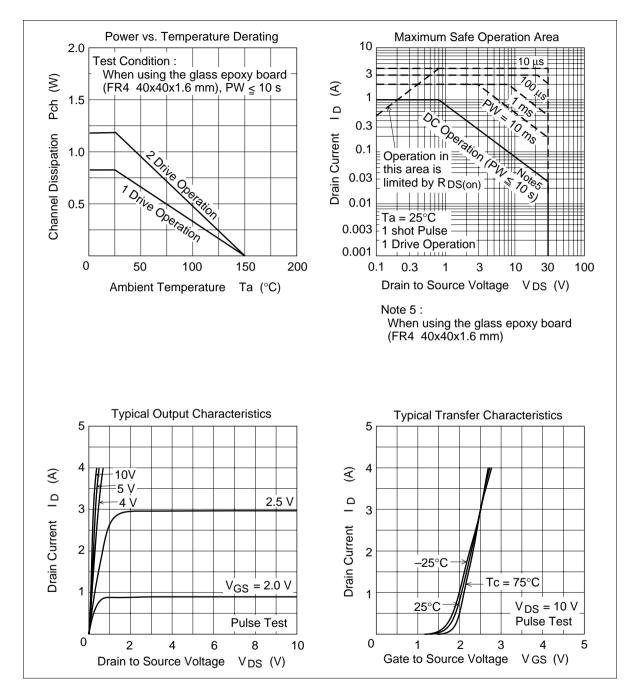
3. 2 Drive operation : When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW≤ 10s

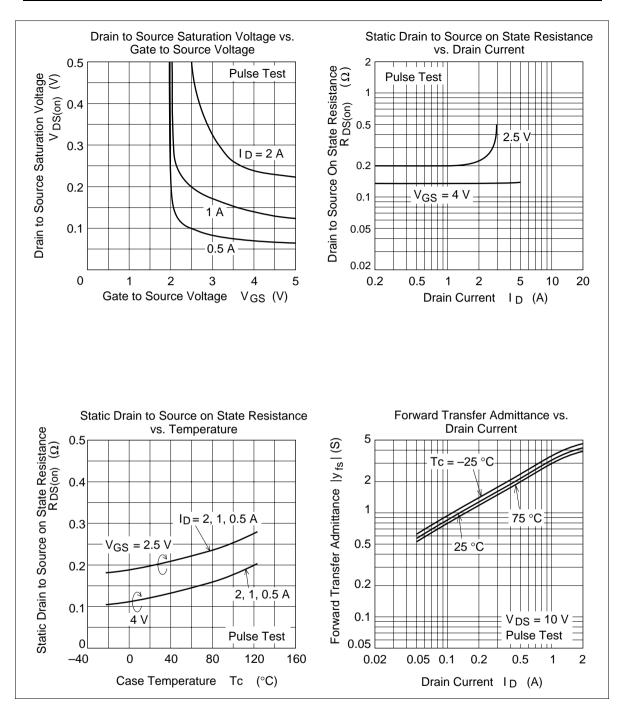
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	30	_	_	V	$I_{\rm D} = 10 \text{ mA}, V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	± 10	_	_	V	$I_{\rm G}=\pm~100~\mu\text{A},~V_{\rm DS}=0$
Gate to source leak current	I _{GSS}	_	_	± 10	μΑ	$V_{GS} = \pm 8 V, V_{DS} = 0$
Zero gate voltege drain current	I _{DSS}	_	_	1	μΑ	$V_{\rm DS} = 30 \ V, \ V_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	0.75	_	1.75	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static drain to source on state	$R_{DS(on)}$	_	0.14	0.2	Ω	$I_{\rm D} = 0.5 \text{ A}, V_{\rm GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R _{DS(on)}	_	0.2	0.3	Ω	$I_{\rm D} = 0.5 \text{ A}, V_{\rm GS} = 4 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}	1.4	2.2		S	$I_{\rm D} = 0.5 \text{ A}, V_{\rm DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	155	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	75	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	—	35		pF	f = 1MHz
Turn-on delay time	t _{d(on)}	—	12	_	ns	$V_{GS} = 4 \text{ V}, \text{ I}_{D} = 0.5 \text{ A}$
Rise time	t,	_	30	_	ns	$V_{DD} \cong 10 \text{ V}$
Turn-off delay time	$t_{d(off)}$	—	35		ns	_
Fall time	t _f	_	25	_	ns	_
Body-drain diode forward voltage	V_{DF}	_	0.81	1.1	V	$I_{F} = 1 \text{ A}, V_{GS} = 0^{Note4}$
Body–drain diode reverse recovery time	t _{rr}	—	35	—	ns	$I_{F} = 1 \text{ A}, V_{GS} = 0$ diF/ dt = 20 A/µs

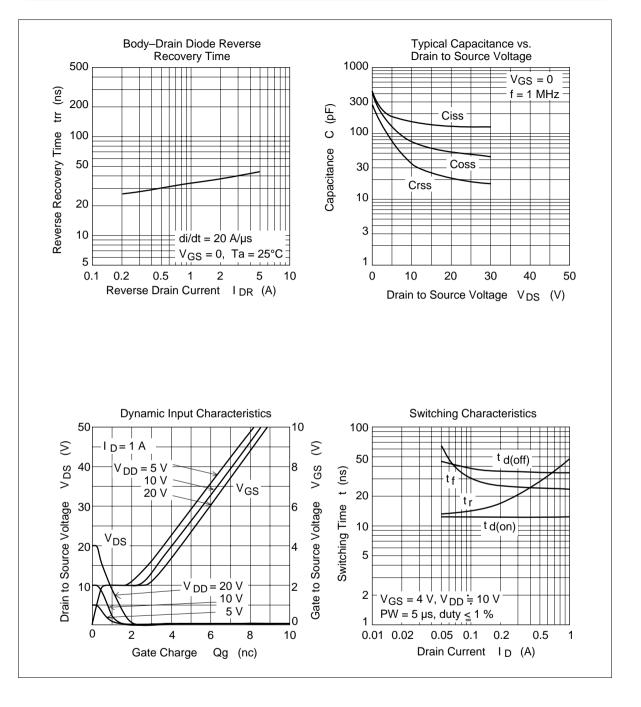
Electrical Characteristics (Ta = 25°C)

Note: 4. Pulse test

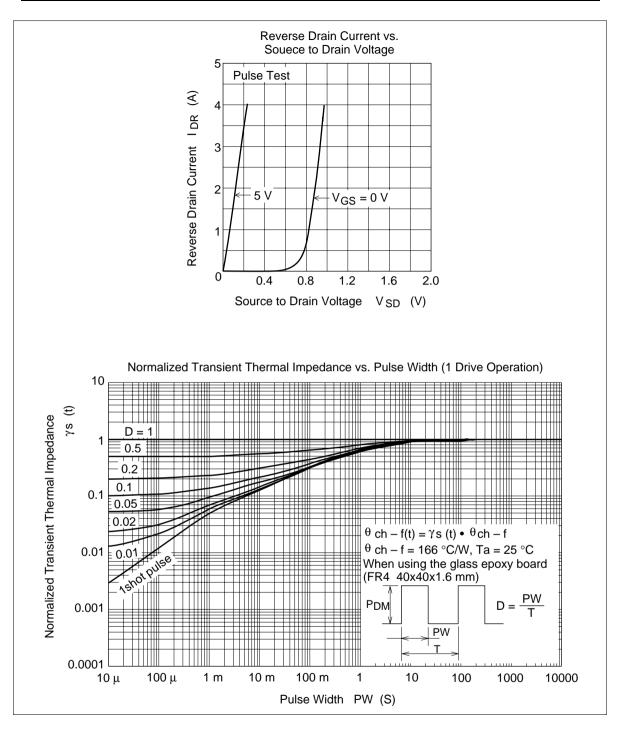
Main Characteristics



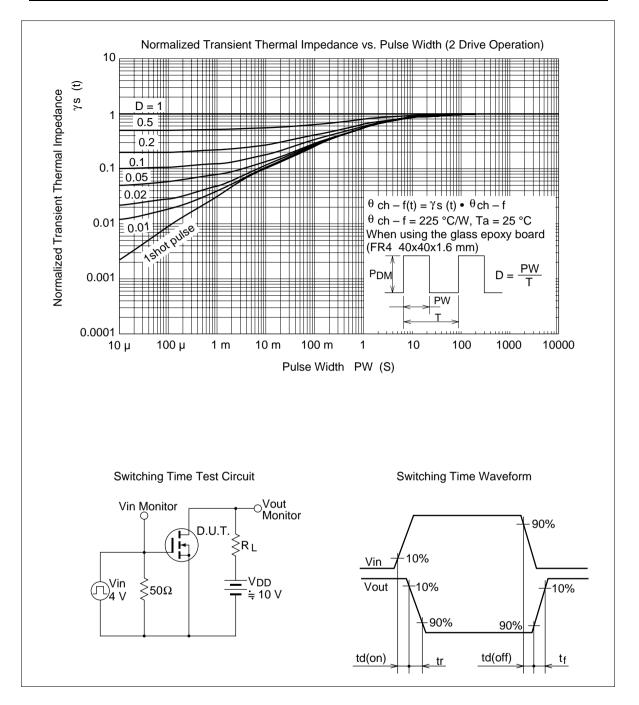




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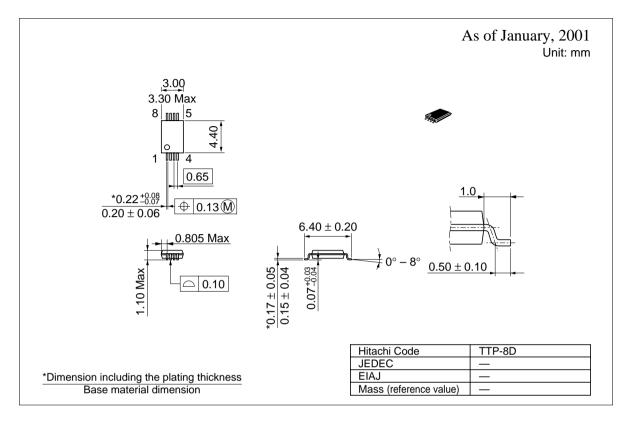


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Package Dimensions



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